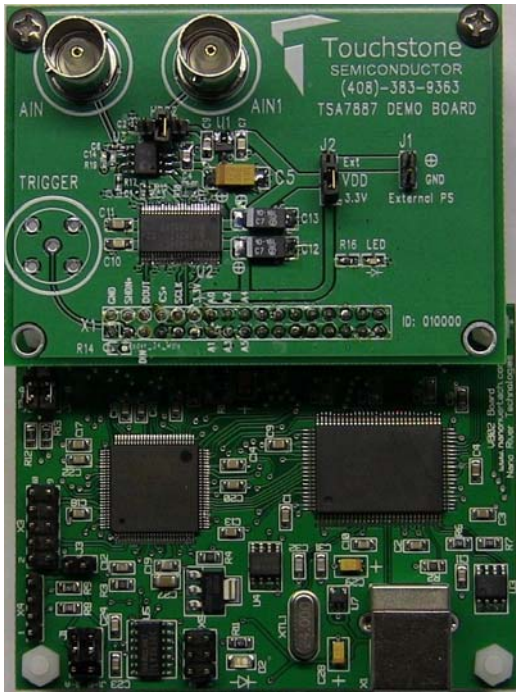
TSA7887 Demo Board

A Micropower, 2-channel, 125-ksps, Serial-Output 12-bit SAR ADC

FEATURES

- Quick and easy Interface to computer for evaluation via Nano River Technologies ViperBoard and USB cable
- Input BNC connection
- On-board +3.3V supply voltage
- On-board TS6001A 2.5V reference voltage
- External power supply connection available
- Fully Assembled and Tested
- 2.2in x 3in 2-layer circuit board



. Figure 1. TSA7887 Evaluation Board with ViperBoard

DESCRIPTION

The demo board for the TSA7887 is a completely assembled and tested circuit board that can be used for evaluating the TSA7887. For easy and quick evaluation of the TSA7887, a Nano River Tech ViperBoard with an onboard microcontroller and FPGA for signal processing is available. The ViperBoard interfaces to any computer via USB port, and a user-friendly Windows OS compatible software is available for evaluation.

The TSA7887 – an alternate source for the AD7887 - is a self-contained, 2-channel, high-speed, micropower, 12-bit analog-to-digital converter (ADC) that operates from a single +2.7V to +5.25V power supply. The TSA7887 is capable of a 125-ksps throughput rate with an external 2MHz serial clock and draws 0.85mA supply current.

Product data sheet and additional documentation can be found at www.silabs.com.

ORDERING INFORMATION

Order Number	Description
TSA7887DB-MSOP	TSA7887 Demo Board (MSOP Package)
TSA7887DB-SOIC	TSA7887 Demo Board (SOIC Package)
TSDA-VB	Nano River Technologies ViperBoard

Figure 2. TSA7887 Evaluation Board (Top View)

Figure 3. TSA7887 Evaluation Board (Bottom View)

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TSA7887 Demo Board

Description

The TSA7887 demo board provides an easy-to-use scheme for evaluating the TSA7887. The default configuration of the demo board in conjunction with the Nano River Technologies ViperBoard is for operation at a supply voltage of V_{DD} =+3.3V.

The TSA7887 demo board includes a 74ALVC164245 signal level translator in order to translate signal levels from V_{DD} of the TSA7887 to +3.3V signal voltage levels necessary for the ViperBoard. The TSA7887 and the ViperBoard are connected via an on-board 34-pin socket (X1).

A separate power supply can be used to power the TSA7887 from +2.7V to +5.25V. The TSA7887 demo can be evaluated without the ViperBoard. A BNC connector is available for input AIN0 and AIN1 of the TSA7887. It is recommended to use a low-noise signal generator in order to acquire optimal SNR and THD results.

The TSA7887's provides one or two analog inputs, AIN0 and AIN1, each with an analog input range from 0 to V_{REF}. In two-channel operation, the analog input range is 0V to V_{DD}. With jumper HDR2, the TS6001A 2.5V external reference or input AIN1 can be selected.

Four power management operating modes are available, namely PM Mode 1- PM Mode 4. Refer to Table 2. In two-channel operation and when using the internal reference, modes 1 and 3 allow a maximum sampling rate of 80ksps while using the ViperBoard.

Figure 5 shows the TSA7887 demo board schematic, Figure 2 and Figure 3 show a picture of the top and bottom of the TSA7887 demo board, respectively. Figure 1 shows a picture of the TSA7887 demo board with the ViperBoard connected.

A USB cable is used to interface the ViperBoard to a PC with Windows Vista 32-bit/Vista 64-bit/XP/7. For the "Touchstone ADC Evaluation Platform GUI" user's manual and for the necessary driver and GUI installer files needed to successfully evaluate the TSA7887 with the ViperBoard, please refer to www.silabs.com.

Quick Start Procedure

Required Equipment

- TSA7887 Demo Board
- Nano River Tech ViperBoard
- USB cable
- Computer with Windows Vista 32-bit/64-bit/XP/7
- ViperBoard driver and GUI software installed to PC(refer to "Touchstone ADC Evaluation Platform GUI" user's manual)
- Low-noise Signal Generator Stanford Research Systems Model DS360 or better w/ BNC cable
- HP 6624A DC Power Supply
- Khron-Hite Corp Model 526 Precision DC Power Supply

In order to evaluate the TSA7887, jumper J2 is to be set to position 2-3 and jumper HDR2 is to be left unconnected. The default configuration for J2 and HDR2 is 1-2 and unconnected, respectively. Refer to Figure 5 and Table 1. The default configuration on the board is for V_{DD} = +3.3V. For this evaluation, we will use an external +5V power supply and an external +5V reference voltage. The following steps are to be performed:

1) Connect the TSA7887 demo board to the ViperBoard via the 34-pin socket (X1).

2) With the HP6624A power supply, connect the positive terminal of the power supply to the positive terminal on J1 and the negative terminal of the power supply to the GND terminal of J1. Set the voltage to +5V and turn it off. With the Khron-Hite 526 DC power supply, connect the positive terminal of the power supply to terminal 2 on HDR2 and connect the negative terminal of the power supply to the GND terminal of J1. Set the voltage to +5V and turn it off.

3) Set the function generator frequency, amplitude, and offset to 10kHz, +4.98V_{PP}, and +2.5V, respectively. Then, connect the BNC cable from the signal generator to the BNC connector AIN0 on the TSA7887 demo board. Set the output of the signal generator to OFF.

4) Turn on the power supplies.

5) To run the GUI, go to *Start* > *All Programs* > *TouchStoneEvalBoard 2.2.11* > *01 Touchstone Evaluation App*. The GUI should appear and on the top right hand side, the status should be "PC disconnected" and "No Type" for demo board type. Both should be highlighted in red indicating that the Nano River Tech ViperBoard and the TSA7887 demo board are not connected.

6) Connect one side of the supplied USB cable to the ViperBoard USB connector. Connect the other side to a USB port on the PC. After approximately three seconds, the status on the GUI screen will switch to "PC Connected" and "TSA7887" indicating the ViperBoard and the TSA7887 demo board were recognized successfully by the software. The green LED on the TSA7887 demo board should be ON and the green LED on the ViperBoard, D2, should be ON.

7) In the TSA7887 ADC section, set PM0 = 1 and REF = 1. In the sampling section of the GUI, enter 125ksps. Use the default FFT length of 8192. Refer to Figure 4 and Table 2 and Table 3.

8) Turn on the signal generator. On the GUI screen, click on "continuous" in the sampling section, and monitor the results on the screen. The top plot shows a samples vs code plot and the bottom plot is an FFT plot. The SNR and THD values should be approximately 71dB and –80dB, respectively. Please refer to Figure 4 for an example of what the results should look like. In this case, the SNR and THD values are 71dB and -83dB,respectively.

Additional Functions

9) As shown in the TSA7887 ADC section of the GUI, the TSA7887 mode of operation can be selected. Please refer to Figure 4 and Table 3.

10) To ensure proper GUI operation, always stop a continuous conversion before changing the state of any of the parameters as described in Table 3. Then, make the necessary jumper connections on the board. Now the parameters can be changed in the GUI according to the connections made on the TSA7887 demo board. Then, select continuous to run.

11) To perform a single conversion, press the "single" button in the sampling section.

12) For details on the Touchstone evaluation platform GUI, refer to the "Touchstone ADC Evaluation Platform GUI" user's manual on our website at: www.silabs.com.

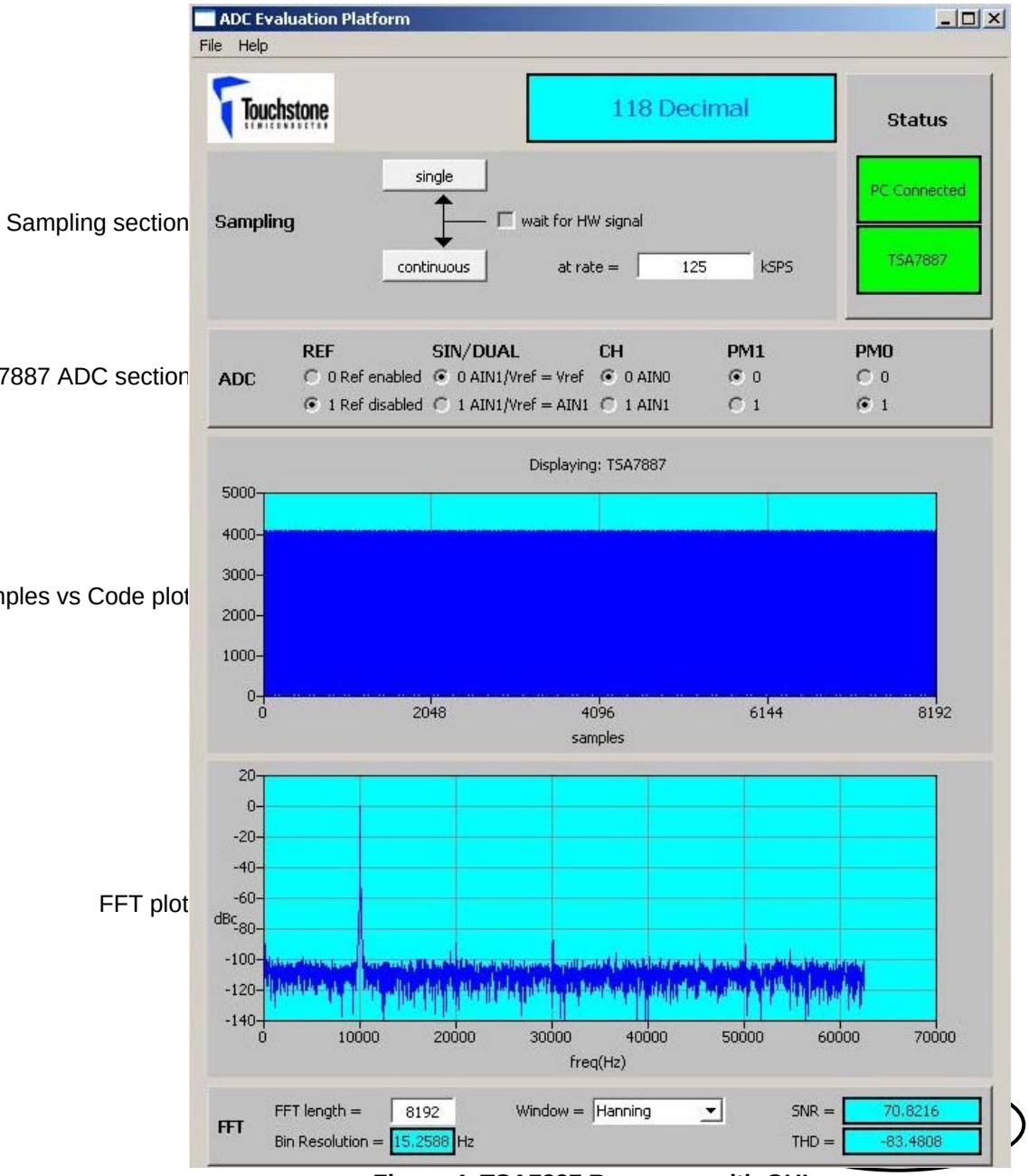


Figure 4. TSA7887 Response with GUI

Evaluation with TSA7887 Demo Board Only

Required Equipment

- TSA7887 Demo Board
- DC Power Supply
- Low-noise Signal Generator Stanford Research Systems Model DS360 or better w/ BNC cable
- Ammeter

In order to evaluate the TSA7887 using the TSA7887 demo board only, additional input signals for the $\overline{CS}$ SCLK, DIN pins will be necessary along with an oscilloscope to monitor the inputs and output. The following steps are to be performed:

1) Set the voltage of the power supply between +2.7V and +5.25V and turn it off. Connect the positive terminal of the DC power supply to pin 12 labeled as “3.3V” on X1, and the ground terminal to pin 2 labeled as “GND”. Refer to Figure 5.

2) Timing signals for the TSA7887 input signals AIN0, AIN1, $\overline{CS}$, DIN, and SCLK should be set according to the desired measurement while following the product datasheet timing specifications. Refer to the TSA7887 product datasheet “Timing Specifications”, “TSA7887 Control Register Description”, and “Description of Operation” section for details. Access to these pins is available through the 34-pin socket, X1, labeled as “SCLK”, “CS*”, “DIN”, and “DOUT”. The corresponding pins on X1 are 10, 8, 5, and 6, respectively.

3) To measure the supply current of the TSA7887, place jumper J2 to position 2-3. Then, apply an external power supply to J1. An ammeter can now be connected in series with the power supply for a static supply current measurement of the TSA7887. The supply current should be less than 900µA. Please refer to Figure 5 and table 1.

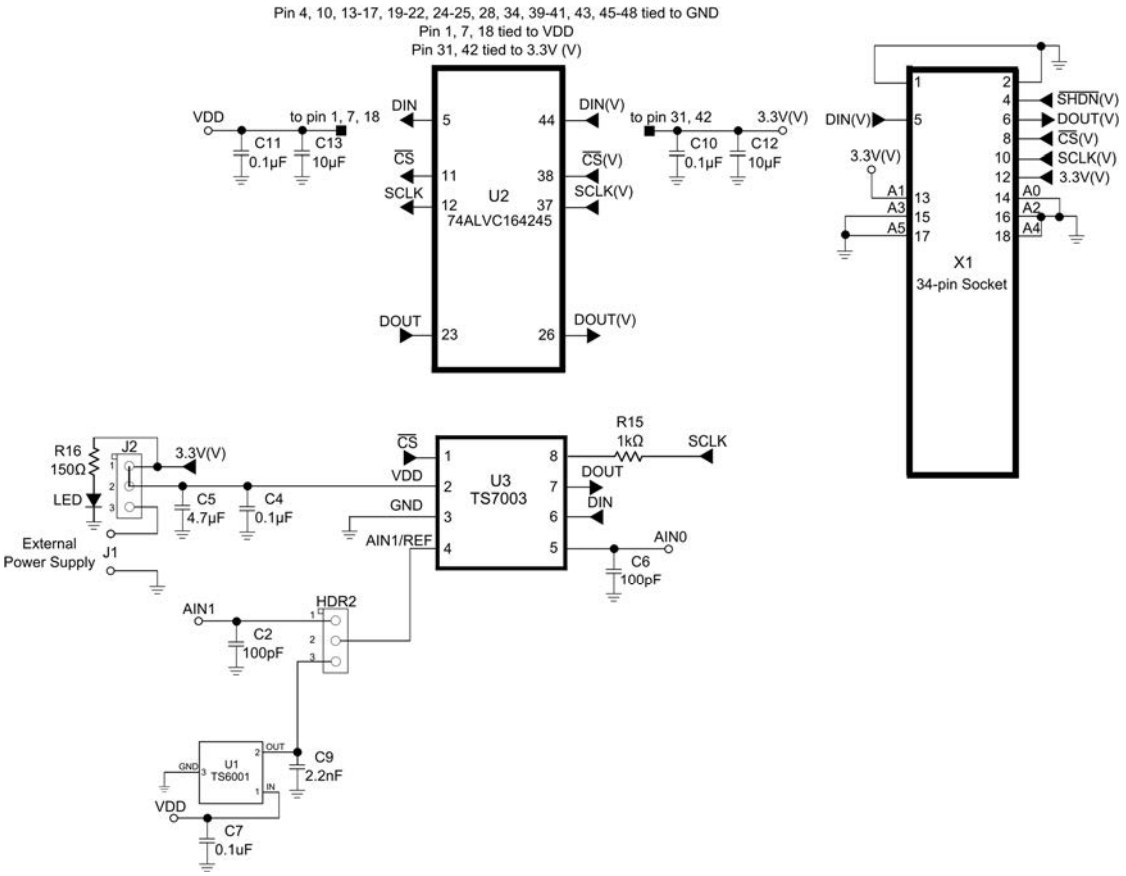


Figure 5. TSA7887 Evaluation Board Circuit

	J1	J2	HDR2	X1	External Power Supplies
TSA7887DB w/ Nano River Tech ViperBoard	NA	1-2 (V_{DD} = 3.3V only)	Refer to Table 3	34-pin socket	0 (REF = 0) 1 (REF =1)
	+2.7V to +5.25V	2-3			1 (REF = 0) 2 (REF =1)
TSA7887 Only	NA	1-2	1-2 (two-channel) 2-3 (on-board reference) Unconnected (external reference or internal reference)	Apply +2.7 to +5.25V to pin 12	1 (REF = 0) 2 (REF =1)
TSA7887 Only (Supply Current Measurement)	+2.7V to +5.25V	2-3	NA	NA	1

Table 1. J1, J2, HDR2, and X1 connections

PM1	PM0	Mode
0	0	**PM Mode 1:** In this operating mode, the TSA7887's power-down mode is enabled if its CS input is a "one" (a "1") and is operating in full-power mode when its CS input is a "zero" (a "0"). Thus, the TSA7887 is powered down on a low-to-high CS transition and is powered up on a high-to-low CS transition.
0	1	**PM Mode 2:** In this operating mode and regardless of the status of any of the logic inputs, the TSA7887 is always fully powered up.
1	0	**PM Mode 3:** In this operating mode, the TSA7887 is automatically powered down at the end of each conversion regardless of the state of the CS input. ADC wake-up time from full shutdown is 5µs and system design should ensure that at least 5µs have elapsed before attempting to perform a conversion in this mode; otherwise, an invalid conversion result may occur.
1	1	**PM Mode 4:** In this operating mode, the TSA7887 is configured for standby operation after conversion. Sections of the TSA7887 are powered down; however, the internal 2.5-V reference voltage remains powered up. While PM Mode 4 is similar to PM Mode 3, PM Mode 4 operation allows the TSA7887 to power up much faster. For optimal performance, the Control Register's REF bit (DB5) should be a "zero" ("0") to ensure the internal reference is enabled/remains enabled.

Table 2. TSA7887's Power Management Operating Modes

Parameter	State	Comments
REF	0	Internal 2.5V reference used. Leave jumper HDR2 unconnected.
	1	Internal 2.5V reference disabled. External TS6001A 2.5V reference can be used.
SIN/DUAL	0	If REF = 0, Pin 4 is a reference voltage pin and internal 2.5V reference is used. If REF = 1, jumper HDR2 should be set to position 2-3 in order to use external TS6001A 2.5V reference voltage.
	1	If REF = 0 or 1, HDR2 is to be set to position 1-2. The device is now in two-channel operation and V_{DD} is used as the reference voltage.
CH	0	AIN0 is selected as the input signal.
	1	AIN1 is selected as the input signal.
PM1	In modes 1 and 3, the maximum sampling rate is 80ksps. Refer to Table 2.	
PM0		

Table 3. TSA7887 Parameter States for evaluation with GUI

DESIGNATION	QTY	DESCRIPTION
C4, C10, C11	3	0.1µF ±10% capacitor (0603)
C12, C13,	2	10µF ±10% capacitor (SMD6032)
C5	1	4.7µF ±10% capacitor (SMD6032)
C2, C6	1	100pF ±10% capacitor (0603)
C9	1	2.2nF ±10% capacitor (0805)
R16	1	150Ω± 1% (0603)
R15	1	1kΩ± 1% (0402)
LED	1	Green LED (0603)
AIN0, AIN1	2	BNC connector
J1	1	2-pin terminal block
X1	1	34-pin socket
J2, HDR2	2	Jumper
U2	1	74ALVC164245 translating transceiver
U1	1	TS6001A
U3	1	TSA7887

Table 4. Component list

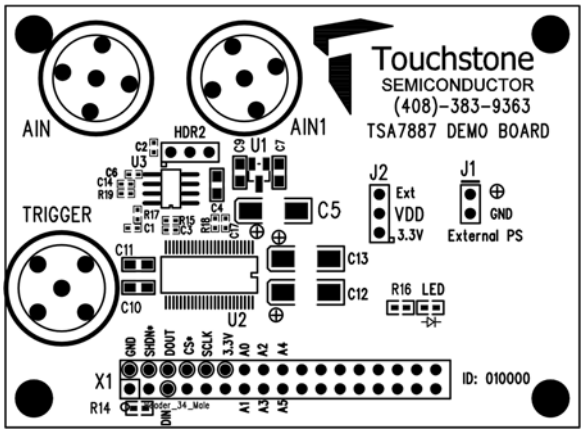


Figure 6. Top Layer Component View

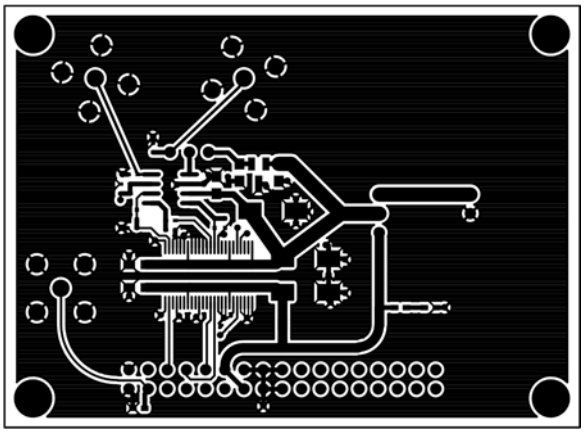


Figure 7. Top Layer Trace View

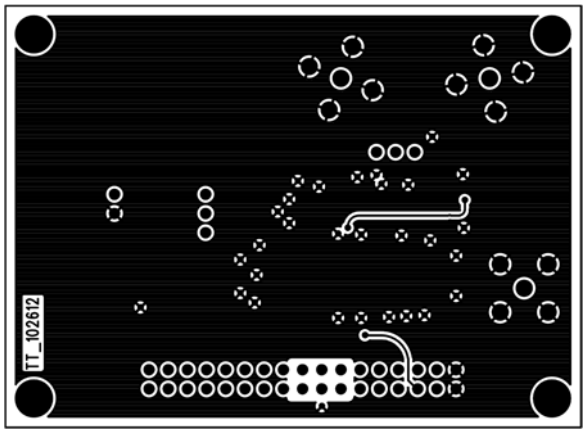


Figure 8. Bottom Layer #1

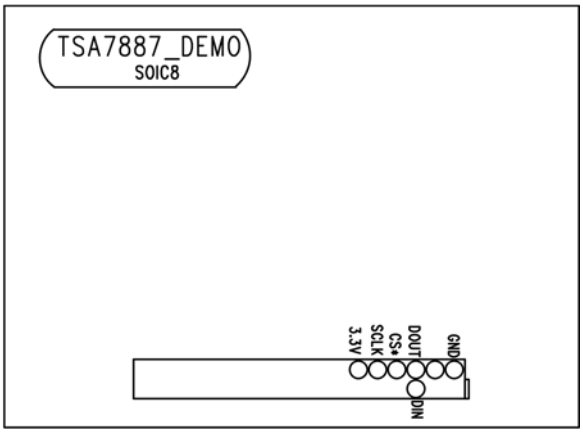
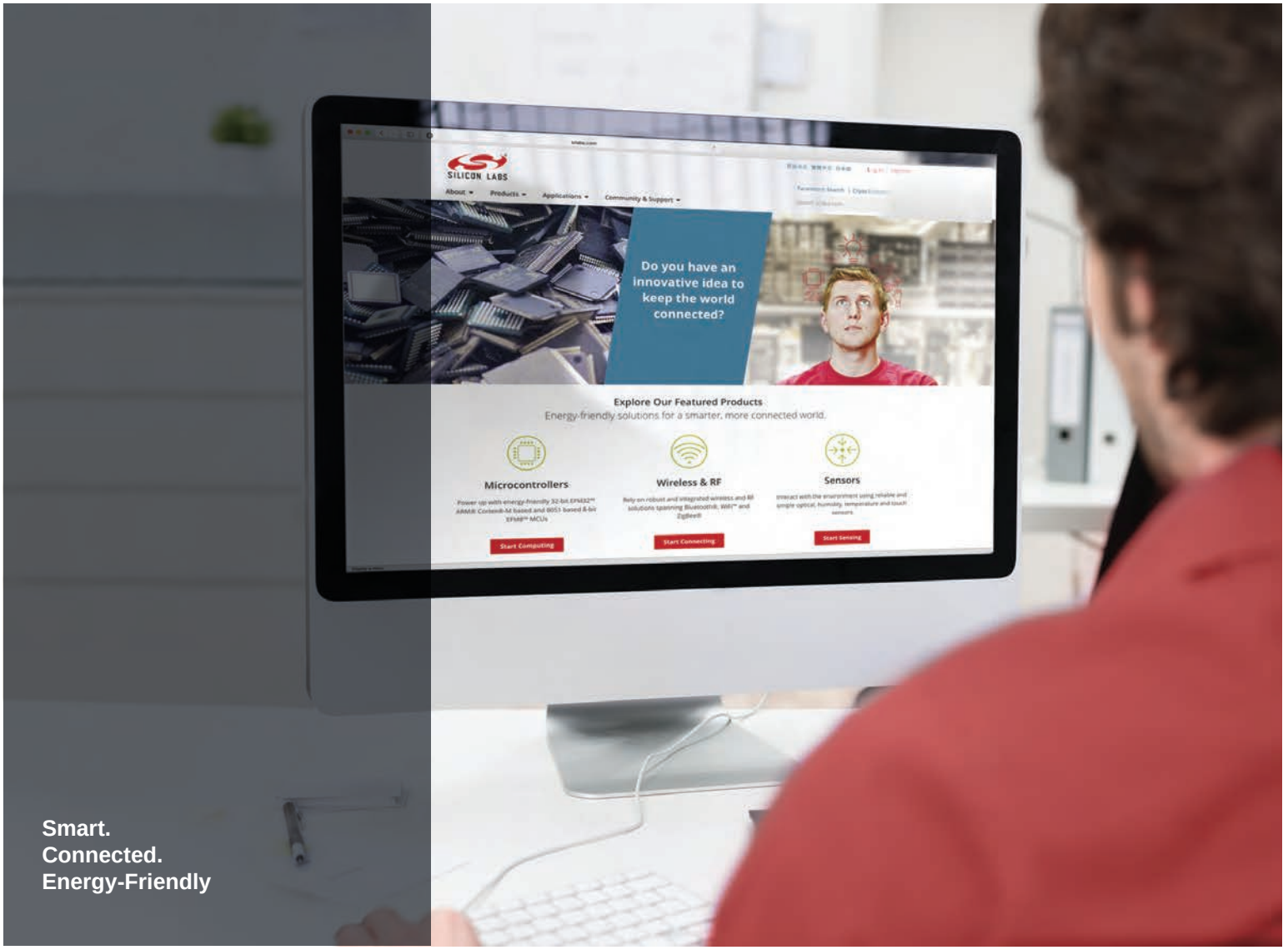


Figure 9. Bottom Layer #2



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